

**PATENTS**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Applicant(s):** Takatoshi Tsujimura, et al.

**Examiner:** Unassigned

**Serial No:** Unassigned

**Art Unit:** Unassigned

**Filed:** Herewith

**Docket:** JP919990117US2 (13633A)

**For:** THIN FILM TRANSISTOR, LIQUID  
CRYSTAL DISPLAY PANEL, AND  
METHOD OF MANUFACTURING  
THIN FILM TRANSISTOR

**Dated:** July 11, 2003

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Sir:

In accordance with 37 C.F.R. §§ 1.97 and 1.98, applicants respectfully submit that the only art which is known to the applicants, in addition to that cited by the Examiner during the prosecution of parent Patent Application No. 09/614,767, were the two (2)

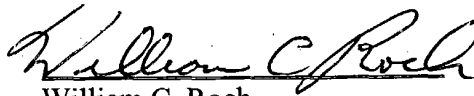
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I hereby certify that this correspondence is being deposited with the United States Postal Service Express Mail Post Office to Addressee service under 37 C.F.R. §1.10 on the date indicated above and is addressed to the Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria, VA 22313-1450.

Dated: July 11, 2003

  
William C. Roch

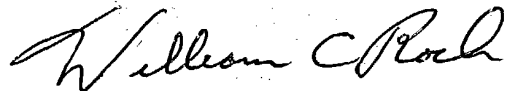
references listed below. Applicant wishes to make these references of record, and are also listed in enclosed PTO-1449.

1. Japanese Patent Publication No. 2516030, issued March 15, 1989; and
2. U.S. Patent Application No. 5,097,297, issued March, 1992.

Reference No. 1 was cited in applicants' Information Disclosure Statement previously submitted in the parent application on October 12, 2000. Reference No. 2 was cited by the Examiner in the Office Action dated April 7, 2003. Since copies of these references are already on file in the parent application, Serial No. 09/614,767, no copies are enclosed herewith.

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R. § 1.97(b), no petition, certification or fee is required.

Respectfully submitted,



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